

**PATENT**

**IN THE UNITED STATES PATENT OFFICE**

In re application of:  
Yongjun J. Hu

Serial No.: Not Assigned

Filed: November 20, 2003 (Concurrently Herewith)

For: **METHOD AND STRUCTURE FOR REDUCING  
RESISTANCE OF A SEMICONDUCTOR DEVICE  
FEATURE**

§  
§ Group Art Unit: Not Assigned  
§  
§ Examiner: Not Assigned  
§  
§ Atty. Docket: 2003-0930.00/US  
§  
§ Paper No. \*  
§  
§

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

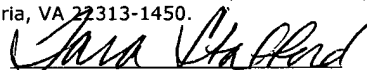
Dear Sir:

**Certificate of Express Mailing (37 CFR §1.10)**

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Date of Deposit: November 20, 2003

I hereby certify that this paper is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR §1.10 on the date indicated above and is addressed to the Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450.

  
Signature

**INFORMATION DISCLOSURE STATEMENT**

In compliance with the duty of disclosure under 37 CFR §1.56, Applicant respectfully requests entry of this Information Disclosure Statement, and that the references listed on the attached Form PTO-1449 be considered by the Examiner and made of record. A copy of any reference required for its consideration is enclosed.

In accordance with 37 CFR §1.97(b), this Information Disclosure Statement is not to be construed as a representation that a search has been made or that no other possible material information as defined in 37 CFR §1.56(a) exists.

The following references are submitted for the Examiner's review:

**U.S. Patent Documents**

| <b>Document No.</b> | <b>Date</b> | <b>Inventor</b> |
|---------------------|-------------|-----------------|
| 6,297,152           | 10/2001     | Itoh et al.     |
| 6,451,644           | 09/2002     | Yu              |
| 6,365,507           | 04/2002     | Hu              |
| 6,486,060           | 11/2002     | Hermes et al.   |

**Other References:**

Copending Application: "Conductive Transistor Structure for a Semiconductor Device and Method for Forming Same", Serial number 10/418,412, Docket Number 2002-1377.00, Filed April 16, 2003.

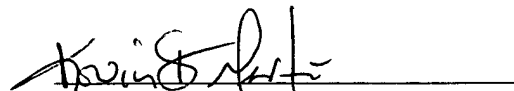
As this Information Disclosure Statement is being submitted before the mailing of a first Office Action on the merits, no fee is due. However, the Commissioner is authorized to charge any required fee to Micron Technology Inc. Deposit Account No.13-3092, Order No. 2003-0930.00/US.

If there are any matters which may be resolved or clarified through telephone interview, the Examiner is respectfully requested to contact Applicant's undersigned agent at the number indicated.

\* \* \* \*

A Form PTO-1449 is enclosed herewith.

Respectfully submitted,



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|   |  |  |                                   |
|---|--|--|-----------------------------------|
| FORM: PTO-1449<br>(REV: 7-80)   | U.S. DEPARTMENT OF COMMERCE<br>PATENT AND TRADEMARK OFFICE | Atty Docket No:<br><b>2003-0930.00/US</b>    | Serial No:<br><b>Not Assigned</b> |
| <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b><br><br>(37 CFR 1.98(b)) <i>(use several sheets if necessary)</i> |  | Applicant:<br><b>Micron Technology, Inc.</b> |                                   |
|   |  | Filing Date:<br><b>November 20, 2003</b>     | Group:<br><b>Not Assigned</b>     |

**U.S. PATENT DOCUMENTS**

| Examiner<br>Initial | Document<br>Number | Date    | Name          | Class | Subclass |  |
|---------------------|--------------------|---------|---------------|-------|----------|--|
| AA                  | 6,297,152          | 10/2001 | Itoh et al.   | 438   | 649      |  |
| AB                  | 6,451,644          | 09/2002 | Yu            | 438   | 230      |  |
| AC                  | 6,365,507          | 04/2002 | Hu            | 438   | 627      |  |
| AD                  | 6,486,060          | 11/2002 | Hermes et al. | 438   | 649      |  |
| AE                  |                    |         |               |       |          |  |
| AF                  |                    |         |               |       |          |  |
| AG                  |                    |         |               |       |          |  |
| AH                  |                    |         |               |       |          |  |
| AI                  |                    |         |               |       |          |  |
| AJ                  |                    |         |               |       |          |  |
| AK                  |                    |         |               |       |          |  |

**FOREIGN PATENT DOCUMENTS**

| Examiner<br>Initial | Document<br>Number | Date | Country | Class | Subclass | Translation              |                          |
|---------------------|--------------------|------|---------|-------|----------|--------------------------|--------------------------|
|                     |                    |      |         |       |          | Yes                      | No                       |
| AL                  |                    |      |         |       |          | <input type="checkbox"/> | <input type="checkbox"/> |
| AM                  |                    |      |         |       |          | <input type="checkbox"/> | <input type="checkbox"/> |
| AN                  |                    |      |         |       |          | <input type="checkbox"/> | <input type="checkbox"/> |

Initial

**OTHER ART** (including author, title, date, pertinent pages, etc.)

|  |    |  |  |
|--|----|--|--|
|  | AO |  | Copending Application: "Conductive Transistor Structure for a Semiconductor Device and Method for Forming Same", Serial number 10/418,412, Docket Number 2002-1377.00, Filed April 16, 2003. |
|  | AP |  |  |
|  | AQ |  |  |
|  | AR |  |  |

|           |                  |
|-----------|------------------|
| Examiner: | Date Considered: |
|-----------|------------------|

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication with applicant.